

P/N	Sec (4-bit 6kHz)	Sec (4-bit 8kHz)	Voice Algorithm	MOQ (K pcs)	MFO (K pcs)	Operating Voltage	ROM	SRAM	I/O	SPI	LDO	CMP	IR	VC 16-level	LCO /CSC	Noise Filter	Fast Clock	Slow Mode	Halt Mode	Int. Rose	Ext. Rose	Voice	Melody	PWM/DAC Option	Ultra PWM	PWM	DAC	Pad Count	NY6_FDB
NY6P025A	25.0	18.8	4/5-bit	OTP		2.0~5.5V	64K x 10	336 x 4	9	v	-	-	v	v	-	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	15	-04 -08 -16 -32
NY6P025J	25.0	18.8	4/5-bit	OTP		2.0~5.5V	64K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6P055J	55.0	41.3	4/5-bit	OTP		2.0~5.5V	136K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6P085J	85.0	63.8	4/5-bit	OTP		2.0~5.5V	208K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6P185J	185.0	138.8	4/5-bit	OTP		2.0~5.5V	448K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-08 -16 -32
NY6P345J	345.0	258.8	4/5-bit	OTP		2.0~5.5V	832K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-16 -32
NY6A008A	8.3	6.3	4/5-bit	19.3K	100K	2.0~5.5V	24K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A011A	11.7	8.8	4/5-bit	19.3K	90K	2.0~5.5V	32K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A018A	18.3	13.8	4/5-bit	17.7K	90K	2.0~5.5V	48K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A025A	25.0	18.8	4/5-bit	17.7K	80K	2.0~5.5V	64K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A035A	35.0	26.3	4/5-bit	15.9K	80K	2.0~5.5V	88K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A045A	45.0	33.8	4/5-bit	15.9K	70K	2.0~5.5V	112K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A055A	55.0	41.3	4/5-bit	14.2K	70K	2.0~5.5V	136K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6A065A	65.0	48.8	4/5-bit	14.2K	60K	2.0~5.5V	160K x 10	336 x 4	8	-	-	-	v	v	8	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	-	v	12-bit	-	14	-04 -08 -16 -32
NY6B005A	5.0	3.8	4/5-bit	15.2K	100K	2.0~5.5V	16K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B008A	8.3	6.3	4/5-bit	15.2K	90K	2.0~5.5V	24K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B011A	11.7	8.8	4/5-bit	15.2K	80K	2.0~5.5V	32K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B018A	18.3	13.8	4/5-bit	14.2K	80K	2.0~5.5V	48K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B025A	25.0	18.8	4/5-bit	14.2K	70K	2.0~5.5V	64K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B035A	35.0	26.3	4/5-bit	12.8K	70K	2.0~5.5V	88K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B045A	45.0	33.8	4/5-bit	12.8K	60K	2.0~5.5V	112K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B055A	55.0	41.3	4/5-bit	11.8K	60K	2.0~5.5V	136K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B065A	65.0	48.8	4/5-bit	11.8K	55K	2.0~5.5V	160K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B075A	75.0	56.3	4/5-bit	10.9K	55K	2.0~5.5V	184K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6B085A	85.0	63.8	4/5-bit	10.9K	50K	2.0~5.5V	208K x 10	336 x 4	16	v	v	v	v	v	16	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	24	-04 -08 -16 -32
NY6C112A	111.7	83.8	4/5-bit	8.9K	36K	2.0~5.5V	272K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-08 -16 -32
NY6C132A	131.7	98.8	4/5-bit	8.9K	34K	2.0~5.5V	320K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-08 -16 -32
NY6C158A	158.3	118.8	4/5-bit	7.5K	32K	2.0~5.5V	384K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-08 -16 -32
NY6C185A	185.0	138.8	4/5-bit	7.5K	30K	2.0~5.5V	448K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-08 -16 -32
NY6C225A	225.0	168.8	4/5-bit	6.2K	26K	2.0~5.5V	544K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-16 -32
NY6C265A	265.0	198.8	4/5-bit	6.2K	24K	2.0~5.5V	640K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-16 -32
NY6C305A	305.0	228.8	4/5-bit	5.3K	22K	2.0~5.5V	736K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-16 -32
NY6C345A	345.0	258.8	4/5-bit	5.3K	20K	2.0~5.5V	832K x 10	336 x 4	24	v	v	v	v	v	24	v	2MHz	v	v	+/- 0.5%	-	6/0	0/6	Auto/Register	v	12-bit	12-bit	34	-16 -32

- Remarks:**
- 1. SPI: SPI Flash interface.
 - 2. LDO: Low Drop Out regulator for PBx or SPI interface.
 - 3. CMP: Comparator.
 - 4. VC 16-level: 16-level digital volume control for both PWM and DAC.
 - 5. LCO/CSC: Large current output / Constant Sink Current output.
 - 6. Noise Filter: Interpolation for output signal. (250kHz Over-Sampling)
 - 7. Slow Clock: lower operating current at slow clock mode.