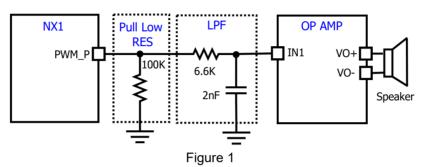
Notice for Using PWM Single-Ended on NX1 EF Series ICs

- **Description:** When the NX1 EF series IC uses PWM Single-Ended mode, an additional hardware circuit is required. The circuit configuration and related recommendations are as follows.
- **Reason:** PWM Single-Ended refers to the NX1 PWM_P single-ended output. The signal is processed through external components and then delivered to the speaker via an operational amplifier. The recommended circuit configuration is shown in Figure 1.



- NX1: NX1 EF series IC bodies.
- Pull Low RES: A 100 kΩ pull-down resistor is recommended. However, ICs with integrated DAC IP (e.g., NX11FS23A and NX12/3FS series) can omit this resistor.
- A 6.6 k Ω resistor and a 2 nF capacitor are recommended for the low-pass filter.
- OP AMP: An operational amplifier used to drive the speaker output.

The setting of software:

• NYIDE: Set AUDIO_OUTPUT_MODE in the .project path)…\src\nx1_config.h to PWM_SE_MODE.

//		
// * Audio Output Mode *		
,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	///////////////////////////////////////	
//====================================	etting	
//====================================	PWM_SE_MODE	// Select PP_OR_PWM_MODE(0)/DAC_MODE(1)/PWM_SE_MODE(2)
#define _AUDIO_RAMP_UP_TIME	8	// Select 0~n, default:8, unit:ms
<pre>#define _AUDIO_RAMP_DOWN_TIME</pre>	4	// Select 0~n, default:4, unit:ms
<pre>#define _AUDIO_AMP_SD_CTL</pre>	DISABLE	<pre>// ENABLE or DISABLE control shut down pin of AMP.</pre>
#define _AUDIO_AMP_SD_CTL_PORT	GPIOA	<pre>// Select PORT GPIOA/GPIOB/GPIOC(only NX1EV)</pre>
#define _AUDIO_AMP_SD_CTL_PIN	8	// Select PIN 0~15 for GPIOA & GPIOB, Select 0~7 for GPIOC(only NX1EV)
#define _AUDIO_AMP_SD_CTL_ACTIVE	ACTIVE_LOW	<pre>// Select shut down pin of AMP is ACTIVE_LOW or ACTIVE_HIGH</pre>
#define _AUDIO_AMP_WAKUP_TIME	30 -	// Select 0~n, default:30,Unit:ms
#define AUDIO AMP SHUTDOWN TIME	10	// Select 0~n, default:10,Unit:ms
#define AUDIO DIGITAL CMPS	ENABLE	// Enable or Disable DAC Mode Digital compensation, only for NX11FS234

• Q-Code: Option \rightarrow Voice Output check the PWM(Single Ended) option.

🔰 Voice Output Optic	n	
Voice Output	PWM (Single Ended	PWM Current Normal Large Ultra
External Amplifie	r Control	Speaker 4Ω (0) 8Ω (1) 16Ω
Pin	PA15	DAC / PWM (Single Ended)
Shutdown Control	Active Low	Ramp Up Time 8 🚭 ms
Shutdown Wait	10 🚔 ms	Ramp Down Time 4 🛃 ms
Wakeup Wait	30 🔦 ms	Interpolation